

Miguel Alexandre Felícia ROCHA

Electrical and Telecommunications and Computers Engineering Student



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Rua dos pescadores nº45

Born November 4th, 2003

SKILLS

Embedded Systems

Hardware/EDA

Software

ArduinoIDE,LoRa, VHDL, Assembly

KiCad, LTSpice, PCB Design, FPGA (Artix-7)

C, Network basics

LANGUAGES

Portuguese

English

INTERESTS

- > Learning

> Weightlifting

> Restoration

EDUCATION

2023 - Currently

Bachelors in Electrical and Telecommunications and Computers Engineering - ISEL

PROJECTS

MULTIPARAMETER MONITORING NETWORK FOR RECYCLING CONTAINERS

2026

Report

For this project i designed and built a Sensor Network, that checks the load inside a container, and sends that information through LoRa

KiCad

ArduinoIDE

C

Network basics

ANEMOMETER WITH THREE-PHASE MOTOR (HDD)

2025

Report

Collaborated in a team to repurpose a Three-Phase HDD motor as an anemometer, utilizing LTSpice for circuit simulation and ArduinoIDE for data processing

OpenSCAD

LTSpice

ArduinoIDE

Oscilloscope Work

VGA INTERFACE CONTROLLED BY AN FPGA

2025

Report

Implemented a real-time video game engine using VHDL, optimizing VGA signal timing for an Artix-7 FPGA

Vivado Xilinx

FPGA

VHDL

WORK EXPERIENCE

September 2025

Kitchen Assistant, HERDADE DOS ADAENS, Campo Maior, Portugal

July 2025

> Preparing and organizing food for the day ahead

> Functioning within a team

> Critical Thinking

> Functioning within a high-stress enviroment

Multitasking